



# CYPRESS IS HIRING!

## ***About Cypress***

Cypress solutions are at the heart of any system that is built to perform: consumer, computation, data communications, automotive, industrial, and solar power. Leveraging a strong commitment to customer service and performance-based process and manufacturing expertise, Cypress's product portfolio includes a broad selection of wired and wireless USB devices, CMOS image sensors, timing solutions, network search engines, specialty memories, high-bandwidth synchronous and micropower memory products, optical solutions, and reconfigurable mixed-signal arrays.

Cypress currently employs approximately 4,500 people world wide, including state of the art fabrication facilities which are driving towards 65nm designs. All employees enjoy an excellent benefits package, including medical and dental, stock options, employee stock purchase plan, and 401(k). For more information, please see <http://www.cypress.com> and follow the link for Careers.

Cypress is an Equal Opportunity Employer

Cypress stock is traded on the New York Stock Exchange under the ticker symbol CY.



## Electrical Design Engineer

*RF/analog/mixed-signal CMOS design*

*Full Time*

*Location: San Diego, CA*

*Consumer and Computation Division*

*Wireless Products Group*

### Job Description

The engineer will work on market-leading chips low power radios. The engineer must be eager to learn and participate in projects from conception to production. The duties include design, simulation, and layout of analog blocks such as bandgaps, regulators, LNAs, Mixers, ADCs, DACs, Crystal Oscillators, and analog filters.

### Job Specific Requirements

Required skills include: knowledge and experience in the design of analog mixed signal integrated circuits, an understanding of MOSFET device physics, modeling (and its limitations) and experience with analog and RF/communications system CAD tools - Cadence (Opus, Spectre, Spectre/RF), SPICE, Mentor (Eldo, Eldo/RF) and Cadence Virtuoso layout tools.

The daily activities also include bench characterization and debug of ICs in the lab, and working with Product and Test Engineers in order to fully characterize the products to get them into mass production. The candidate will be expected to work closely with cross functional team members to solve technical problems and to be able to take direction for independent work.

### Preferred Education/Experience

BSEE or advanced degree (MSEE, PhD) in Electrical Engineering or Computer Engineering; 3.0 G.P.A. in major, out of 4.0 scale.

2 to 10 years experience in the design of RF/analog CMOS mixed signal integrated circuits.

*Please send resumes to Robert Eilers: [rye@cypress.com](mailto:rye@cypress.com)*



## Software Engineer

*Location: Beaverton, OR*

*Full Time*

*Consumer and Computation Division*

### Job Description

Will be responsible for developing, improving and supporting various S/W products in CCD (Consumer and Computation Division). Immediate responsibilities include developing next generation PSoC software.

### Job Specific Requirements

Strong Object oriented programming skills in C+/C# or Java, strong fundamentals in data structures and algorithms, working knowledge in developing software on UNIX and /or Windows platforms. The candidate must have good communications skills, good problem solving skills, good work ethics and must be a team player.

### Minimum Education & Work Experience

BSEE/MSEE/BSCS/MSCS or equivalent.

1-2yr. experience is a plus.

*Please send resumes to Robert Eilers: [rye@cypress.com](mailto:rye@cypress.com)*



## Electrical Design Engineer/Verification Engineer

*Location: Beaverton, OR/Boise, ID/Lynnwood, WA*

*Full Time / Intern (6 mo. preferred)*

*Consumer and Computation Division*

### Job Description

Cypress is seeking qualified students to work on our next generation of Programmable System on Chips (PSoC™). This new family of devices will extend the current PSoC line which is used in a wide range of products including MP3 players, HD TVs, tennis shoes, and wireless devices. The PSoC family covers 8 bit, 32 bit, as well as wireless applications. Specific contributions will involve development of cycle accurate C++ models. These models are generated from a detailed chip specification.

### Job Specific Requirements

The minimum skill set includes C++, Perl, and a basic understanding of digital logic design. Development is done in both Windows and Linux/Solaris. The C++ code development and debug is done in MS Visual Studio 2005, actual simulations and comparison with the Chip is done in Linux/Solaris. The ideal candidate will have experience implementing SOC designs which include a uP, memory system, interrupt control, and various peripheral interfaces such as I2C, CAN, or USB. For any candidate the most important is a strong C++ background with some experience using Perl or other scripting languages.

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